



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al. Art Unit: 2812
Serial No.: 09/941,158 Examiner:
Filed : August 28, 2001 Stacy Whitmore
Assignee : Intel Corporation
Title : MODEL-BASED LOGIC DESIGN

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

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Applicant : William R. Wheeler et al.
Serial No.: 09/941,158
Filed : August 28, 2001

Attorney's Docket No.:10559-596001


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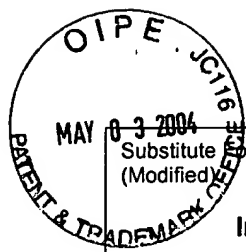
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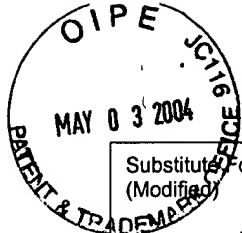
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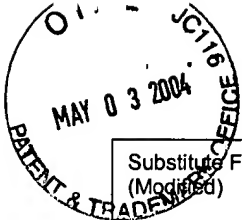
U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	RE 38,059	04/2003	Yano, et al.			
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	ABB	5,841,663	11/1998	Sharma, et al.			
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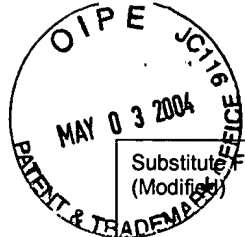


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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	AZZZ	WO 99/39268	08/1999	WIPO				

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	AAAAA	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
	ABBBB	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
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	AEEEE	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
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	ALLLL	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

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